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Inventor:

John Mattson

Title:

Methods Of Forming Magnetoresistive Memory Devices (as

amended)

Assignee:

Micron Technology, Inc.

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional of co-pending application Serial No. 10/196,484 filed July 15, 2002. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are 37 C.F.R. §1.98(d) and MPEP §609(2). No admission is concerned. made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: July 31, 2003 Attorney:

David G. Latwesen, Ph.D. Reg. #38,533

WELLS ST. JOHN P.S.

Form PTO-1449				ATTY. DOCKET NO MI22-2376		PRIORITY SERIAL NO. 10/196,484							
LIST OF ART CITES (Use several sheet							APPLICANT John Muttson						
				,,			PRIORITY FILING DATE July 15, 2002			PRIORITY GROUP			
U.S. PATENT DOCUMENTS													
*Examiner Initial		Document Number		Date	Name			Class Sub		ass	Filing Date If Appropriate		
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)													
	AR Website: http://philly.com/contnquirer/2000/08/10/tech_life/MRAM10.html; The Philadelphia Inquirer; "Comput good, swift boot", by Leslie J. Nicholson; August 10, 2000 (reprinted 7/24/01).										"Computers	to get a	
EXAMINER	L					DATE CONSIDERE	ONSIDERED						
			considered, whether o		in conformance	with MPEP 609; Drav	v line through citation i	f not in conf	ormance	and no	t considered.	Include	